2SD1775A

Silicon NPN triple diffusion planar type

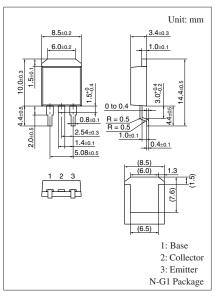
For high-speed switching and high current amplification ratio

Features

- \bullet High forward current transfer ratio h_{FE}
- \bullet Satisfactory linearity of forward current transfer ratio h_{FE}
- N type package enabling direct soldering of the radiating fin to the printed circuit board, etc. of small electronic equipment.

Parameter	Symbol	Rating	Unit				
Collector-base voltage (Emitter open)	V _{CBO}	100	V				
Collector-emitter voltage (Base open)	V _{CEO}	80	V				
Emitter-base voltage (Collector open)	V _{EBO}	6	V				
Collector current	I _C	2	А				
Peak collector current	I _{CP}	4	А				
Base current	IB	0.5	А				
Collector power dissipation	P _C	25	W				
$T_a = 25^{\circ}C$]	1.3					
Junction temperature	Tj	150	°C				
Storage temperature	T _{stg}	-55 to +150	°C				





Note) Self-supported type package is also prepared.

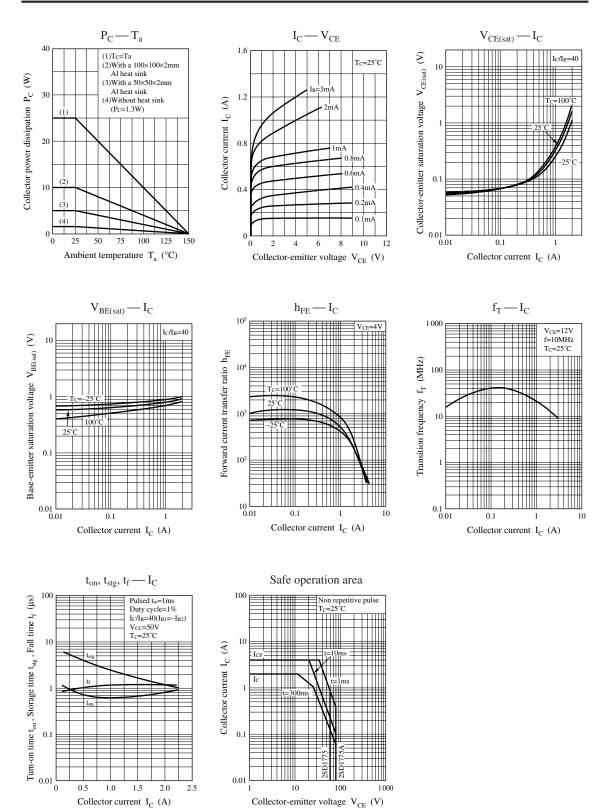
Electrical Characteristics $T_C = 25^{\circ}C \pm 3^{\circ}C$

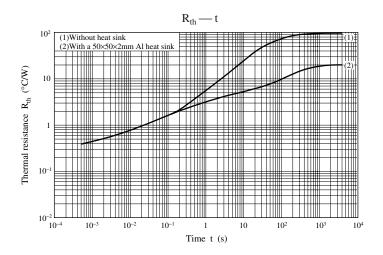
Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-emitter voltage (Base open)	V _{CEO}	$I_{\rm C} = 25 \text{ mA}, I_{\rm B} = 0$	80			V
Collector-base cutoff current (Emitter open)	I _{CBO}	$V_{CB} = 100 \text{ V}, I_E = 0$			100	μΑ
Collector-emitter cutoff current (Base open)	I _{CEO}	$V_{CE} = 40 \text{ V}, I_B = 0$			100	μΑ
Emitter-base cutoff current (Collector open)	I _{EBO}	$V_{EB} = 6 V, I_C = 0$			100	μΑ
Forward current transfer ratio *	h _{FE}	$V_{CE} = 4 V, I_C = 300 mA$	500		1500	_
Collector-emitter saturation voltage	V _{CE(sat)}	$I_{\rm C} = 1 \text{ A}, I_{\rm B} = 25 \text{ mA}$			1.0	V
Base-emitter saturation voltage	V _{BE(sat)}	$I_{\rm C} = 1 \text{ A}, I_{\rm B} = 25 \text{ mA}$			1.2	V
Transition frequency	f _T	$V_{CE} = 12 \text{ V}, I_C = 200 \text{ mA}, f = 10 \text{ MHz}$		40		MHz
Collector output capacitance	C _{ob}	$V_{CB} = 10 \text{ V}, I_E = 0, f = 1 \text{ MHz}$		30		pF
(Common base, input open circuited)						
Turn-on time	t _{on}	$I_C = 1 A$		0.6		μs
Strage time	t _{stg}	$I_{B1} = 25 \text{ mA}, I_{B2} = -25 \text{ mA}$		2.5		μs
Fall time	t _f	$V_{CC} = 50 V$		1.0		μs

Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors. 2. *: Rank classification

Rank	R	Q
$h_{\rm FE}$	500 to 1 000	800 to 1500

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